



Receipt

ATTORNEY DOCKET NO. 5649-944

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Ahn et al.

Serial No.: 10/074,309

Group Art Unit: 2818

Filed: February 11, 2002

For: INTEGRATED CIRCUIT DEVICES HAVING DELAY CIRCUITS FOR
CONTROLLING SETUP/DELAY TIMES OF DATA SIGNALS THAT ARE
PROVIDED TO MEMORY DEVICES AND METHODS OF OPERATING SAME

Date: March 22, 2002

Commissioner for Patents
Washington, DC 20231

Attn: Office of Initial Patent Examination
Customer Service Center

REQUEST FOR CORRECTED FILING RECEIPT

Sir:

In reviewing the Official Filing Receipt (copy attached) for the above referenced application, Applicant notes that inventor Ahn's address incorrectly appears as "Kyungki-do, CHINA".

Applicant respectfully requests that inventor Ahn's address be corrected to read "Kyungki-do, REPUBLIC OF KOREA" and that a corrected Filing Receipt be issued.

If any extension of time for the accompanying response or submission is required, Applicant requests that this be considered a petition therefor. The Commissioner is hereby authorized to charge any additional fee, which may be required, or credit any refund, to our Deposit Account No. 50-0220.

Customer Number:



20792

Respectfully submitted,

D. Scott Moore
Registration No. 42,011

In re Ahn et al.

Serial No. 10/074,309

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Certificate of Mailing under 37 CFR 1.8

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on March 22, 2002.

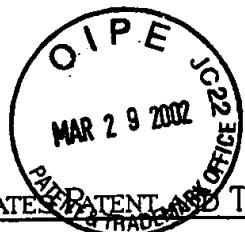
Date

Signature: Evelyn B. Casey

Typed or Printed Name of Person Signing Certificate: Evelyn B. Casey

Date of Signature: March 22, 2002

DSm

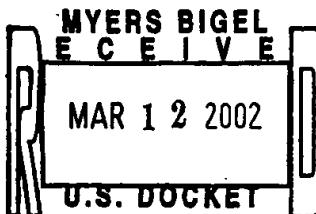


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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/074,309	02/11/2002	2818	1190	5649-944	7	31	6

20792
 MYERS BIGEL SIBLEY & SAJOVEC
 PO BOX 37428
 RALEIGH, NC 27627



CONFIRMATION NO. 6441

FILING RECEIPT



OC000000007608433

Date Mailed: 03/08/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2001-8141 02/19/2001

If Required, Foreign Filing License Granted 03/08/2002

Projected Publication Date: 08/22/2002

Non-Publication Request: No

Early Publication Request: No

Title

Integrated circuit devices having delay circuits for controlling setup/delay times of data signals
 that are provided to memory devices and methods of operating same

Preliminary Class

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Bib Data Sheet

CONFIRMATION NO. 6441

SERIAL NUMBER	FILING DATE	CLASS	GROUP ART UNIT	ATTORNEY DOCKET NO.
10/074,309	02/11/2002 RULE	365	2824	5649-944

APPLICANTS

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** CONTINUING DATA *****

NONE JHA

** FOREIGN APPLICATIONS *****

REPUBLIC OF KOREA 2001-8141 02/19/2001 *JHA*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/08/2002

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	KOREA, REPUBLIC OF	DRAWING 7	31	6
Verified and Acknowledged	<i>J. H. Ahn</i> Examiner's Signature	<i>JHA</i> Initials			

ADDRESS

20792

TITLE

Integrated circuit devices having delay circuits for controlling setup/delay times of data signals that are provided to memory devices and methods of operating same

FILING FEE RECEIVED 1190	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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